IGBT MODULE

MBL400E33D

Silicon N-channel IGBT

FEATURES

- * High thermal fatigue durability.(delta Tc=70oC, N>30,000cycles)
- * High speed, low loss IGBT module.
- * Low noise due to built-in free-wheeling diode
- ultra soft fast recovery diode(USFD).
- * Low driving power due to low input capacitance MOS gate.
- * High reliability, high durability module.
- * Isolated heat sink (terminal to base).

ABSOLUTE MAXIMUM RATINGS (T_C=25°C)

Item		Symbol	Unit	MBL400E33D
Collector Emitter Voltage)	V _{CES}	V	3,300
Gate Emitter Voltage		V _{GES}	V	±20
Collector Current	DC	Ic	A	400
Collector Current	1ms	I _{CRM}		800
Forward Current	DC	l _F	Α	400
Forward Current	1ms	I _{FRM}	^	800
Junction Temperature		T _{vj op}	°C	-40 ~ +125
Storage Temperature		T _{stg}	∘C	-40 ~ +125
Isolation Voltage		V _{ISO}	V _{RMS}	6,000(AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	N·m	2/22 (1)
	Mounting (M6)	-	IN-III	6 (2)

Notes: (1) Recommended Value 1.8±0.2/22±1N·m

(2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS 1)IGBT+FWD

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I _{CES}	mΑ	-	-	12	V _{CE} =3,300V, V _{GE} =0V, T _{vi} =25°C
Gate Emitter Leakage Current	I _{GES}	nΑ	-500	-	+500	$V_{GE}=\pm 20V, V_{CE}=0V, T_{vj}=25^{\circ}C$
Collector Emitter Saturation Voltag	e V _{CEsat}	V	3.5	4.2	5.0	I _C =400A, V _{GE} =15V, T _{vj} =125°C
Gate Emitter Threshold Voltage	$V_{GE(th)}$	V	4.5	6.0	7.0	V _{CE} =10V, I _C =400mA, T _{vj} =25°C
Input Capacitance	Cies	nF	-	35	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, T _{vj} =25°C
Internal Gate Resistance	R _{G(int)}	Ω	-	3.6	-	$V_{CE}=10V$, $V_{GE}=0V$, $f=100kHz$, $T_{vj}=25$ °C
Turn On Delay Time	t _{d(on)}		-	0.5	-	V _{CC} =1,650V, I _C =400A
Rise Time	t _r		1.0	1.9	3.1	L _S =150nH
Turn Off Delay Time	t _{d(off)}	t _{d(off)} μS		2.0	2.6	$R_G=10\Omega$ (3)
Fall Time	t _f		0.5	1.0	2.5	$V_{GE}=\pm 15V, T_{vj}=125^{\circ}C$
Forward Voltage Drop	V _F	V	2.0	2.5	3.0	I _F =400A, V _{GE} =0V, T _{vj} =125°C
Reverse Recovery Time	t _{rr}	μS	-	0.6	-	V _{CC} =1,650V, I _F =400A, (4) L _S =150nH, T _{vi} =125°C
Thermal Impedance	R _{th(j-c)}	K/W	-	-	0.026	Junction to case
FWD	R _{th(j-c)}	IX/VV	-		0.052	Juniciion to case
Contact Thermal Impedance	R _{th(c-f)}	K/W	-	-	0.016	Case to fin

2) Chopper Diode

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I _{AKS}	mΑ	-	-	12	V _{AK} =3,300V, T _{vj} =25°C
Forward Voltage Drop	V _F	V	2.2	2.7	3.2	I _F =400A,T _{vj} =125°C at main terminals (Terminal resistance:0.5mΩ typical)
Reverse Recovery Time	t _{rr}	μS	0.2	0.6	1.1	V_{CC} =1,650V, I_F =400A, (4) L_S =150nH, T_{vi} =125°C
Thermal Impedance	R _{th(j-c)}	K/W	-	-	0.052	Junction to case
Contact Thermal Impedance	R _{th(c-f)}	K/W	-	-	0.016	Case to fin

Notes: (3) R_G value is a test condition value for evaluation, not recommended value.

Please, determine the suitable R_G value after the measurement of switching waveforms (overshoot voltage, etc.)with appliance mounted.

(4)Counter arm IGBT VGE=-15V

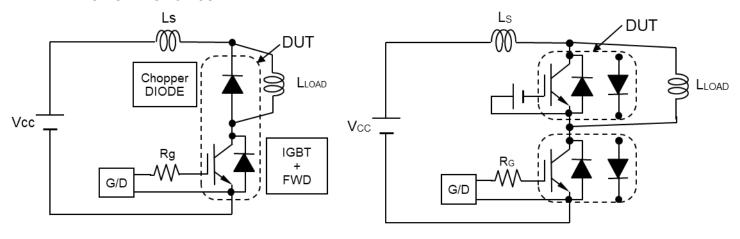
- * Please contact our representatives at order.
- * For improvement, specifications are subject to change without notice.
- * For actual application, please confirm this spec sheet is the newest revision.



IGBT MODULE Spec.No.IGBT-SP-06008 R7 P 2

MBL400E33D

DEFINITION OF TEST CIRCUIT



Test for IGBT and Chopper Diode

Test for FWD

Fig.1 Switching test circuit

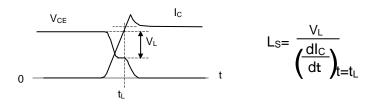


Fig.2 Definition of stray inductance

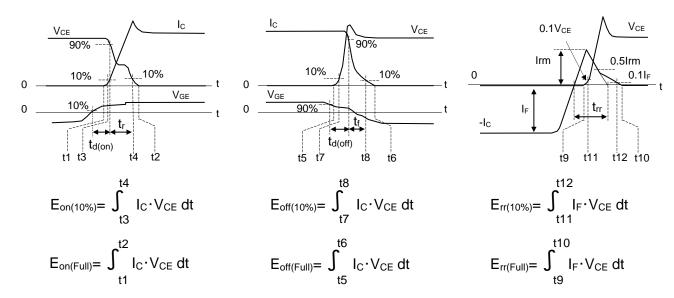
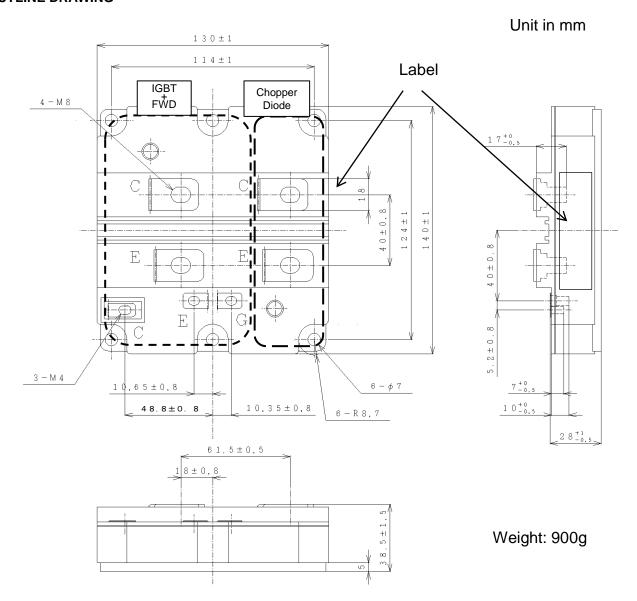
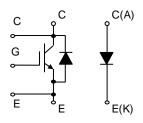


Fig.3 Definition of switching loss

OUTLINE DRAWING

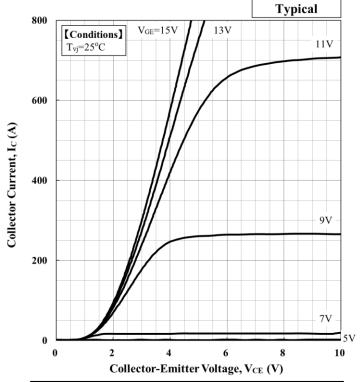


CIRCUIT DIAGRAM



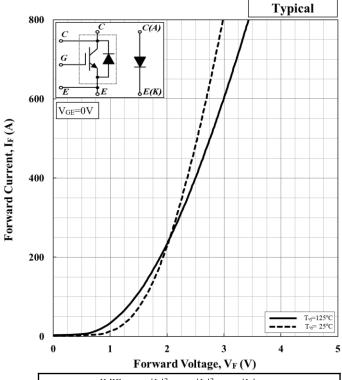
IGBT MODULE

MBL400E33D



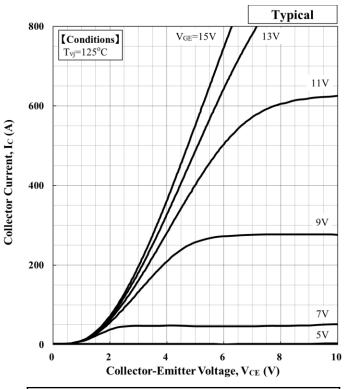
$V_{CE}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$						
Temp.[°C]	V _{GE} [V]	a_3	a_2	a_1	a_0	
25	15	4.31E-09	-7.35E-06	7.41E-03	1.33E+00	

Collector Current vs. Collector Emitter Voltage



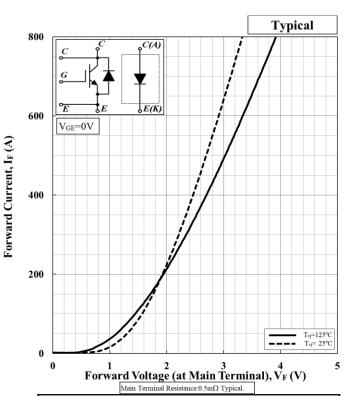
$V_{F}[V] = a_{3} \cdot I_{F} ^{3} + a_{2} \cdot I_{F} ^{2} + a_{1} \cdot I_{F} + a_{0}$						
Temp.[°C]	a_3	a_2	a_1	a_0		
25	3.06E-09	-5.35E-06	4.60E-03	1.19E+00		
125	3.42E-09	-6.22E-06	5.99E-03	8.92E-01		

Forward Voltage of free-wheeling diode



$V_{CE}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C]	V _{GE} [V]	a_3	a_2	a_1	a_0		
125	15	6.25E-09	-1.02E-05	1.04E-02	1.28E+00		

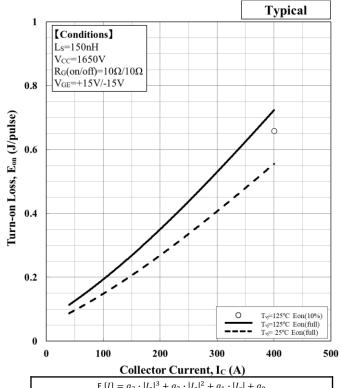
Collector Current vs. Collector Emitter Voltage



$V_{F}[V] = a_{3} \cdot I_{F} ^{3} + a_{2} \cdot I_{F} ^{2} + a_{1} \cdot I_{F} + a_{0}$				
Temp.[°C]	a_3	a_2	a_1	a_0
25	2.70E-09	-4.82E-06	4.93E-03	1.10E+00
125	3.24E-09	-5.94E-06	6.56E-03	8.29E-01
	1 17-14	f Cl		

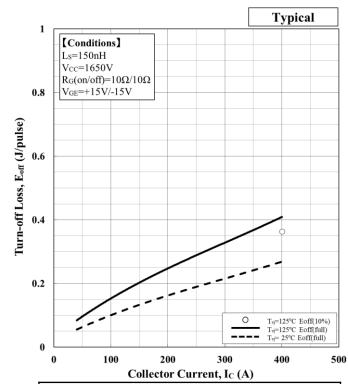
Forward Voltage of Chopper diode





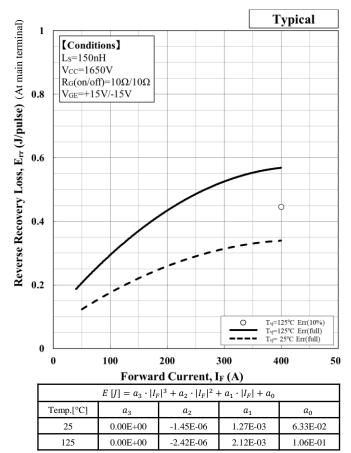
$E[J] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$					
Temp.[°C]	a_3	a_2	a_1	a_0	
25	-1.07E-09	1.51E-06	8.25E-04	5.18E-02	
125	-1.40E-09	1.97E-06	1.08E-03	6.75E-02	

Turn-on loss vs. Collector current

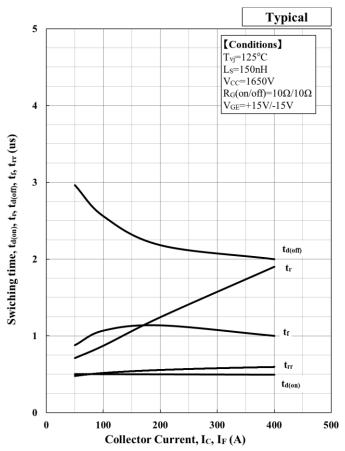


	$E[J] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$					
Temp.[°C]	a_3	a_2	a_1	a_0		
25	1.48E-09	-1.34E-06	9.17E-04	2.05E-02		
125	2.26E-09	-2.04E-06	1.40E-03	3.12E-02		

Turn-off loss vs. Collector current

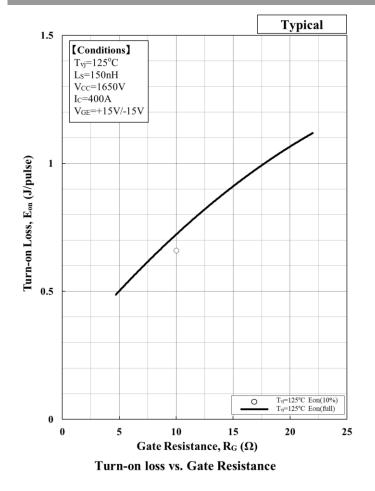


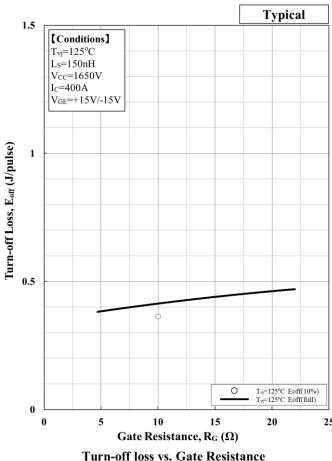
Recovery loss vs. Forward Current(Chopper Diode)

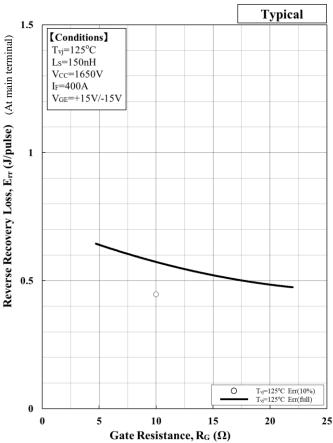


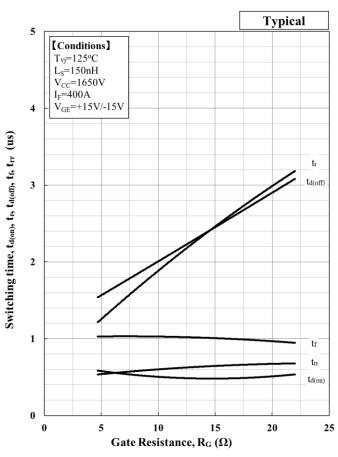
Switching time vs. Collector Current







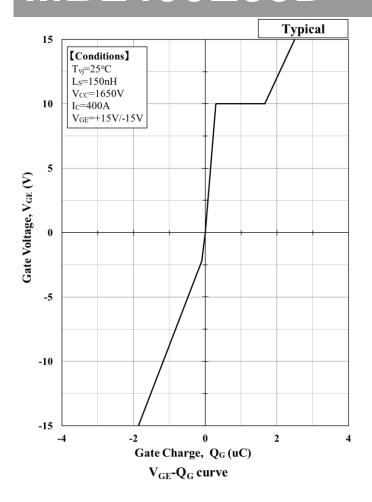


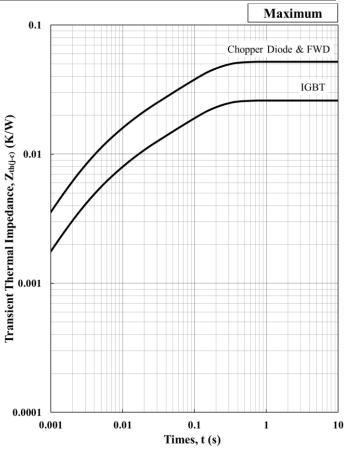


Reverse Recovery loss vs. Gate Resistance (Chopper Diode)

Switching time vs. Gate Resistance







Transient Thermal Impedance Curve

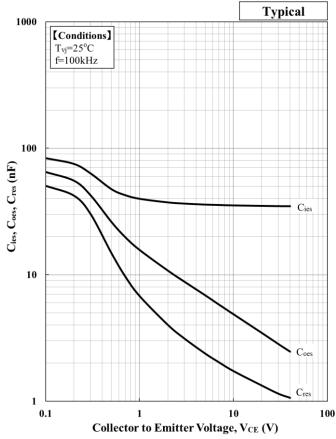
Foster model lumped circuit constant

n	1	2	3	4
R th, IGBT [n]	1.73E-02	2.67E-03	6.98E-05	6.00E-03
C th, IGBT [n]	6.43E+00	8.25E-01	2.23E+00	1.67E+00
R th, Diode [n]	3.46E-02	5.53E-03	1.29E-04	1.17E-02
C th, Diode [n]	3.20E+00	3.99E-01	1.21E+00	8.53E-01

Cauer model lumped circuit constant

n	1	2	3	4
R th, IGBT [n]	1.67E-03	4.95E-03	6.20E-03	1.32E-02
C th, IGBT [n]	4.14E-01	1.52E-01	1.47E+00	6.10E+00
R th, Diode [n]	3.53E-03	9.69E-03	1.24E-02	2.64E-02
C th, Diode [n]	2.07E-01	7.23E-02	7.50E-01	3.02E+00





Capacitance vs. Collector to Emitter Voltage

Material declaration

Please note the following materials are contained in the product, in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder



HITACHI POWER SEMICONDUCTORS

Notices

- 1. Since mishandling of semiconductor devices may cause malfunctions, please be sure to read "Precautions for Safe Use and Notices" in the individual brochure before use.
- 2. When designing an electronic circuit using semiconductor devices, please do not exceed the absolute maximum rating specified for the device under any external fluctuations. And for pulse applications, please also do not exceed the "Safe Operating Area (SOA)".
- 3. Semiconductor devices may sometimes break down by accidental or unexpected surge voltage, so please be careful about the safety design such as redundant design and malfunction prevention design which don't cause the damage expand even if they break down.
- 4. In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, life-support-related medical equipment, fuel control equipment and various kinds of safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement. Or consult with Hitachi's sales department staff. (When semiconductor devices fail, as a result the semiconductor devices or wiring, wiring pattern may smoke, ignite, or the semiconductor devices themselves may burst.)
- 5. A semi-processed article is done now using solder which contains lead inside the semiconductor devices. There is possibility of the regulation substance depend on the applied models, so please check before using.
- 6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
- 7. The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact with Hitachi power semiconductor sales department for the latest version of this data sheets.
- 8. For handling other than described in this manual, follow the handling instructions (IGBT-HI-00002).

For inquiries relating to the products, please contact nearest representatives that is located "Inquiry" portion on the top page of a home page.

Hitachi power semiconductor home page address http://www.hitachi-power-semiconductor-device.co.jp/ http://www.hitachi-power-semiconductor-device.co.jp/en/



HITACHI POWER SEMICONDUCTORS

Usage I

- HPSD warrants that the HPSD products have the specified performance according to the respective specifications at the time of its sale. Testing and other quality control techniques of the HPSD products by HPSD are utilized to the extent HPSD needs to meet the specifications described in this document. Not every device of the HPSD products is specifically tested on all parameters, except those mandated by relevant laws and/or regulations.
- 2. Following any claim regarding the failure of a product to meet the performance described in this document made within one month of product delivery, all the products in relevant lot(s) shall be retested and re-delivered. The HPSD products delivered more than one month before such a claim shall not be counted for such response.
- 3. HPSD assumes no obligation nor makes any promise of compensation for any fault which should be found in a customer's goods incorporating the products in the market. If a product failure occurs for reasons obviously attributable to HPSD and a claim is made within six months of product delivery, HPSD shall offer free replacement or payment of compensation. The maximum compensation shall be the amount paid for the products, and HPSD shall not assume responsibility for any other compensation.
- 4. HPSD reserves the right to make changes in this document and to discontinue mass production of the relevant products without notice. Customers are advised to confirm specification of the product of inquiry before purchasing of the products that the customer desired. Customers are further advised to confirm before purchasing of such above products that the product of inquiry is the latest version and that the relevant product is in mass production status if the purchasing of the products by the customer is suspended for one year or more.
- 5. When you dispose of HPSD products and/or packing materials, comply with the laws and regulations of each country and/or local government. Conduct careful preliminary studies about environmental laws applying to your products such as RoHS, REACH. HPSD shall not assume responsibility for compensation due to contravention of laws and/or regulations.
- 6. HPSD shall not be held liable in any way for damages and infringement of patent rights, copyright or other intellectual property rights arising from or related to the use of the information, products, and circuits in this document.
- 7. No license is granted by this document of any patents, copyright or other intellectual property rights of any third party or of HPSD.
- 8. This document may not be reprinted, reproduced or duplicated, in any form, in whole or in part without the express written permission of HPSD.
- 9. You shall not use the HPSD products (technologies) described in this document and any other products (technologies) manufactured or developed by using them (hereinafter called "END Products") or supply the HPSD products (technologies) and END Products for the purpose of disturbing international peace and safety, including (i) the design, development, production, stockpiling or any use of weapons of mass destruction such as nuclear, chemical or biological weapons or missiles, (ii) the other military activities, or (iii) any use supporting these activities. You shall not sell, export, dispose of, license, rent, transfer, disclose or otherwise provide the HPSD products (technologies) and END Products to any third party whether directly or indirectly with knowledge or reason to know that the third party or any other party will engage in the activities described above.

 When exporting, re-export transshipping or otherwise transferring the HPSD products (technologies)

and END Products, all necessary procedures are to be taken in accordance with Foreign Exchange and Foreign Trade Act (Foreign Exchange Act) of Japan, Export Administration Regulations (EAR) of US, and any other applicable export control laws and regulations promulgated and administered by the governments of the countries asserting jurisdictions over the parties or transaction.

