3300V Silicon N-channel IGBT F version with SiC Diode

#### **FEATURES**

\* Soft switching & low conduction loss IGBT:

Soft low-injection punch-through

High conductivity IGBT with advanced trench MOS gate.

- \* Low driving power due to low input capacitance.
- \* Ultra low recovery loss with SiC diode.
- \* High Current rate Package.
- \* Low stray inductance.
- \* RoHS

#### **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub>=25°C)

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Item		Symbol	Unit	MBN1200F33F-C3
Collector Emitter Voltage	V <sub>CES</sub>	V	3,300	
Gate Emitter Voltage	V <sub>GES</sub>	V	±20	
Collector Current	DC	Ic	Λ	1,200
	1ms	I <sub>CRM</sub>	1 A	2,400
Forward Current	DC	I <sub>F</sub>	Λ	1,200
	1ms	I <sub>FRM</sub>	A	2,400
Junction Temperature	•	T <sub>vj op</sub>	°C	-40 ~ +150
Storage Temperature		T <sub>stg</sub>	°C	-40 ~ +150
Isolation Voltage		V <sub>ISO</sub>	V <sub>RMS</sub>	6,000(AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	N·m	2/15 (1)
	Mounting (M6)	-	IN-III	6 (2)

Notes: (1) Recommended Value 1.8±0.2/15<sup>+0</sup>-3N·m (2) Recommended Value 5.5±0.5N·m

#### **ELECTRICAL CHARACTERISTICS**

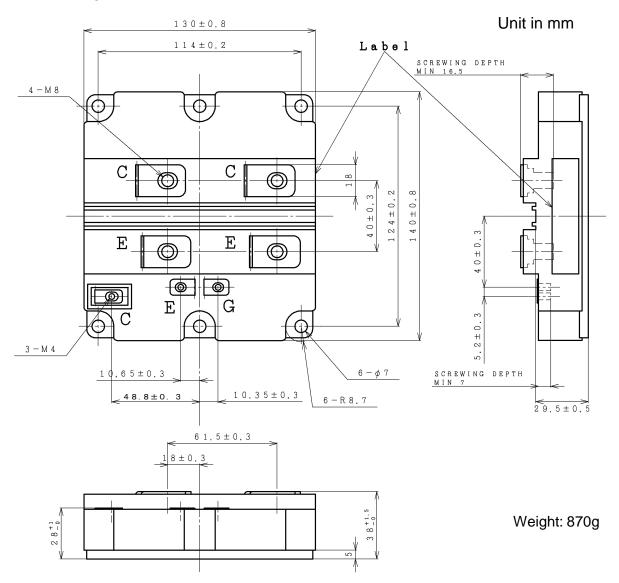
Item		Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Collector Emitter Cut-Off Current		I <sub>CES</sub>	mA	-	-	12	V <sub>CE</sub> =3,300V, V <sub>GE</sub> =0V, T <sub>vi</sub> =25°C
				-	25	-	$V_{CE}=3,300V, V_{GE}=0V, T_{vj}=150^{\circ}C$
Gate Emitter Leakage Current		I <sub>GES</sub>	nΑ	-500	-	+500	$V_{GE}=\pm 20V, V_{CE}=0V, T_{vj}=25^{\circ}C$
Collector Emitter Saturation Voltage		V <sub>CEsat</sub>	V	-	2.85	-	I <sub>C</sub> =1,200A, V <sub>GE</sub> =15V, T <sub>vj</sub> =150°C
Gate Emitter Threshold Voltage		$V_{GE(th)}$	V	-	6.5	-	$V_{CE}=10V$ , $I_{C}=1,200$ mA, $T_{vj}=25$ °C
Input Capacitance		Cies	nF	-	88	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^{\circ}C$
Internal Gate Resistance		R <sub>G(int)</sub>	Ω	-	1.9	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^{\circ}C$
Turn On Delay Time		t <sub>d(on)</sub>	μ\$	-	0.9	-	V <sub>CC</sub> =1,800V, I <sub>C</sub> =1,200A
Rise Time		t <sub>r</sub>		-	0.2	-	L <sub>S</sub> =100nH
Turn Off Delay Time		t <sub>d(off)</sub>		-	2.2	-	$R_G(\text{on/off})=6.8\Omega/8.2\Omega$ (3)
Fall Time		t <sub>f</sub>		-	2.0	-	$V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$
Peak Forward Voltage Drop		V <sub>F</sub>	V	-	4.6	-	I <sub>F</sub> =1,200A, V <sub>GE</sub> =0V, T <sub>vj</sub> =150°C
Reverse Recovery Time		t <sub>rr</sub>	μS	-	0.1	-	V <sub>CC</sub> =1,800V, I <sub>F</sub> =1,200A, L <sub>S</sub> =100nH
							T <sub>vj</sub> =150°C
Turn On Loss		Eon	J/P	-	1.4	-	V <sub>CC</sub> =1,800V, I <sub>C</sub> =1,200A, L <sub>S</sub> =100nH
Turn Off Loss		E <sub>off</sub>	J/P	-	2.2	-	$R_G(\text{on/off})=6.8\Omega/8.2\Omega$ (3)
Reverse Recovery Loss		Err	J/P	-	(0.1)	-	$V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$
Stray inductance module		L <sub>SCE</sub>	nΗ	-	10	-	
Short Circuit Pulse Width		t <sub>sc</sub>	μS	10	-	-	V <sub>CC</sub> =2,200V,Ls=100nH
							$R_G(\text{on/off})=6.8/82\Omega, V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$
Thermal Impedance	IGBT	R <sub>th(j-c)</sub>	K/W	-	-	0.010	Junction to case
	FWD	R <sub>th(j-c)</sub>		-	-	0.017	
Contact Thermal Impedance		R <sub>th(c-f)</sub>	K/W	-	0.008	-	Case to fin

Notes: (3)  $R_G$  value is a test condition value for evaluation, not recommended value. Please, determine the suitable  $R_G$  value by measuring switching behaviors.

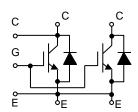
- \* Please contact our representatives at order.
- \* For improvement, specifications are subject to change without notice.
- \* For actual application, please confirm this spec sheet is the newest revision.
- \* ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

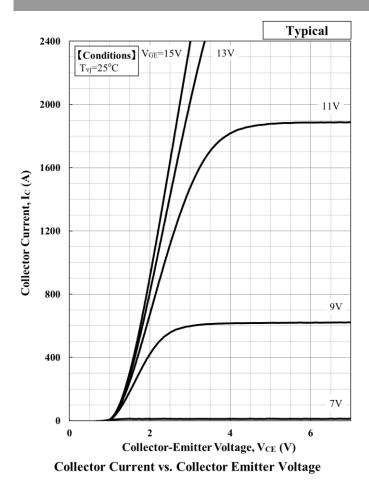


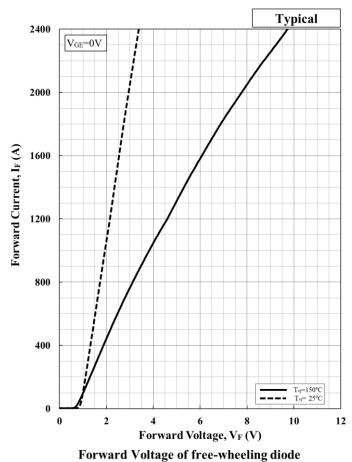
#### **OUTLINE DRAWING**

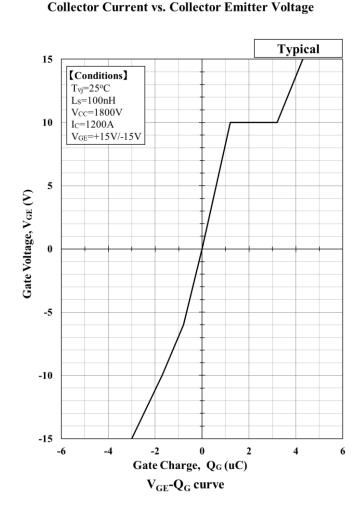


#### **CIRCUIT DIAGRAM**

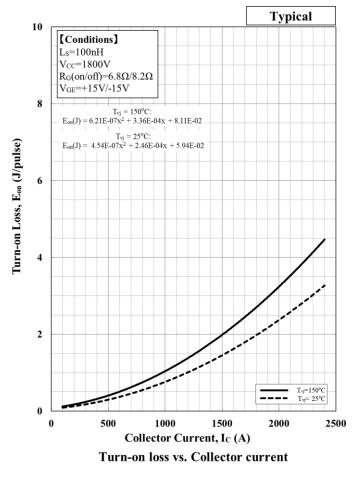


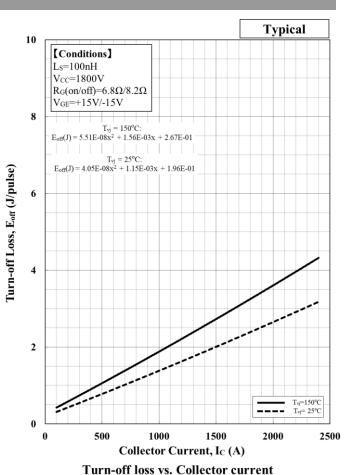


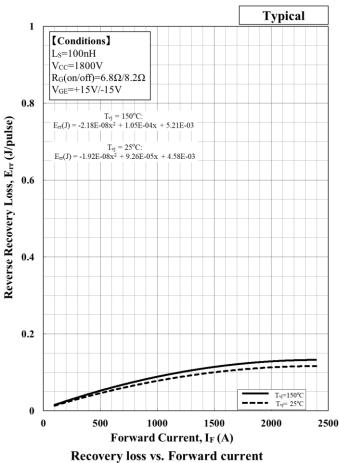


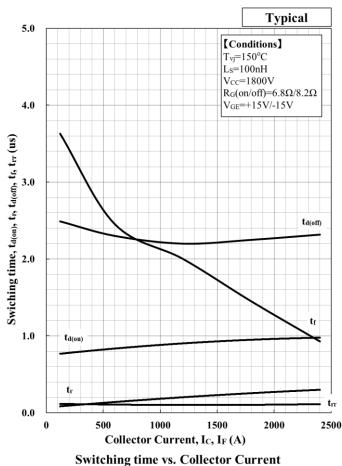


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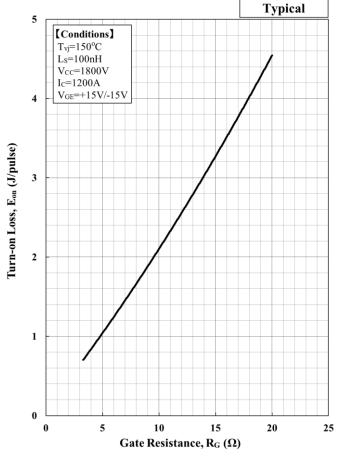




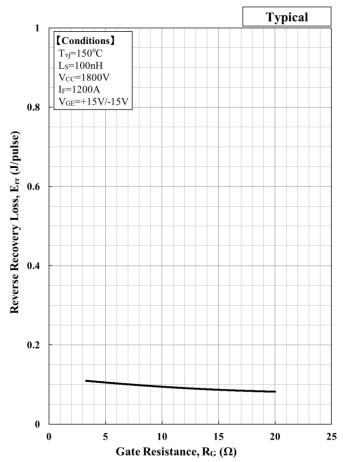




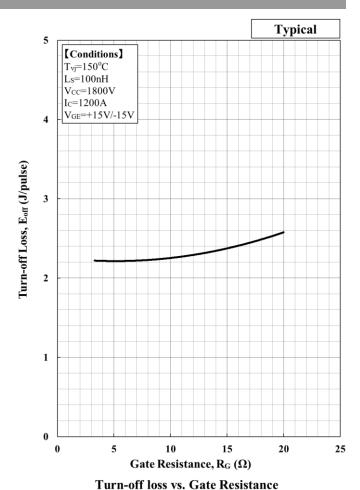
HITACHI Inspire the Next



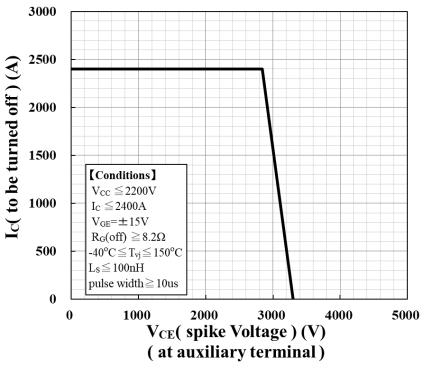
Turn-on loss vs. Gate Resistance

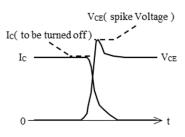


Recovery loss vs. Gate Resistance



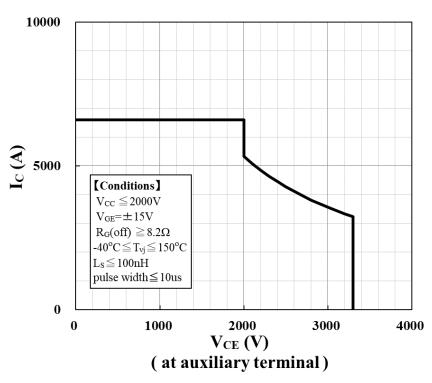






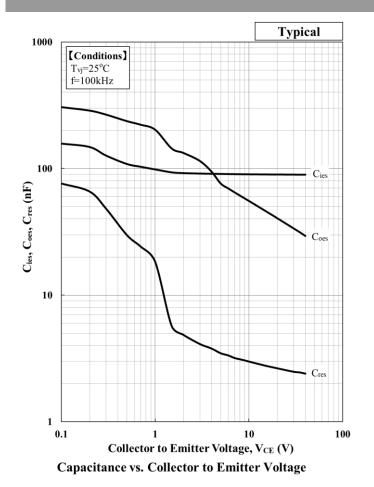
Definition of RBSOA waveform

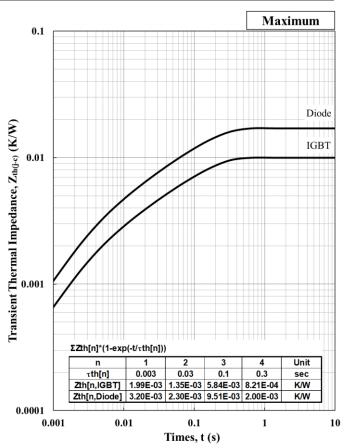
Reverse Bias Safe Operation Area (RBSOA)



**Short cuicuit Safe Operation Area (SCSOA)** 







**Transient Thermal Ipedance Curve** 

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